

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	Examiner: Thao X Le
)	
Inventor(s): Justin Brask)	Art Unit: 2814
Patent Application No.: 10/748,559)	
Filed: December 29, 2003)	Confirmation No.: 9088
For: CMOS Device with Metal and Silicide Gate)	
<u>Electrodes and a Method for Making It</u>)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**CHANGE OF CORRESPONDENCE ADDRESS,
POWER OF ATTORNEY, AND REVOCATION OF PREVIOUS POWERS**

Please direct all future correspondence regarding the above-referenced patent application to Rahul D. Engineer Reg. No. 47,548, c/o Blakely, Sokoloff, Taylor, & Zafman LLP, Customer Number 45209, 1279 Oakmead Parkway, Sunnyvale, California 94085, and direct all telephone calls to the same at (408) 720-8300.

INTEL CORPORATION

(Name of Assignee)

("assignee"), a company organized under the laws of Delaware, having a place of business at 2200 Mission College Blvd, Santa Clara, CA 95052, hereby states that to the best of assignee's knowledge and belief it is the assignee of the entire right, title, and interest in and to the above-referenced patent application and represents that the undersigned is a representative authorized and empowered to sign on behalf of the assignee. Upon information and belief, the assignment documents that evidence the placement of title in the assignee were recorded in the U.S. Patent and Trademark Office. The Assignment was recorded in the Patent and Trademark Office at Reel 014859 Frame 0790, or when the Assignment has not yet been recorded, a copy thereof is attached.

Pursuant to 37 C.F.R. §§ 1.36 and 3.71, the assignee hereby revokes all powers of attorney previously given and appoints the practitioners associated with Customer Number 45209 as the assignee's respective patent attorneys and patent agents, with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

Pursuant to 37 C.F.R. § 3.71, the assignee hereby states that prosecution of the above-referenced patent application is to be conducted to the exclusion of the inventor(s).

Assignee of Interest: Intel Corporation
Dated: 5-16-08 By: Pam Matlock (Type or Print)
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